CLAIMS

What is claimed is:

| 1 | 1. An apparatus for attenuating undesirable high frequency signals in | | |
|----|--|--|--|
| 2 | an alternating current (AC) power signal comprising: | | |
| 3 | a capacitor; | | |
| 4 | a control device having two terminals and a control electrode, | | |
| 5 | coupled to the capacitor; | | |
| 6 | a control circuit for sensing a high potential on the capacitor when | | |
| 7 | it is disconnected from the AC power signal, coupled to the control | | |
| 8 | electrode of the control device; and | | |
| 9 | a variable resistor coupled between one of the two terminals and | | |
| 10 | the control electrode of the control device for surge protection from | | |
| 11 | unusually high voltages at the power source, the variable resistor causing | | |
| 12 | the control device to conduct in the presence of the unusually high | | |
| 13 | voltage. | | |
| 1 | 2. The apparatus of claim 1, wherein the variable resistor is a varistor. | | |
| 1 | 3. The apparatus of claim 2, wherein the varistor has cross-bar | | |
| 2 | characteristics. | | |
| 1 | 4. The apparatus of claims 1 or 3, wherein the control device is a | | |
| 1 | | | |
| 2 | TRIAC. | | |
| 1 | 5. The apparatus of claim 4, wherein the two terminals of the control | | |
| 2 | device are an anode terminal and cathode terminal, and wherein the | | |
| 3 | variable resistor is coupled between the control electrode and the anode. | | |

| 1 | о. | The apparatus of claim 2 including a resistor coupled in series with | | |
|----|-------------------------------------|---|--|--|
| 2 | the co | the control device. | | |
| 1 | 7. | An apparatus comprising: | | |
| 2 | | a capacitor for coupling to receive an alternating current power | | |
| 3 | signal | signal for attenuating signals having a frequency higher than the | | |
| 4 | funda | fundamental frequency of the alternating current power signal; | | |
| 5 | | a control device having a gate and two terminals, the two terminals | | |
| 6 | being coupled to the capacitor; and | | | |
| 7 | | a variable resistor being coupled between the gate and one of the | | |
| 8 | termin | terminals of the control device for causing the control device to conduct | | |
| 9 | when | when the alternating current power signal is unusually high; | | |
| 10 | | whereby voltage surges in the alternating current signal are | | |
| 11 | shorte | d. | | |
| 1 | 8. | The apparatus defined by claim 7, wherein the control device is a | | |
| 2 | TRIAC | TRIAC. | | |
| 1 | 9. | The apparatus defined by claim 8, wherein the variable resistor is a | | |
| 2 | varisto | varistor. | | |
| 1 | 10. | The apparatus defined by claim 9, wherein a resistor is coupled in | | |
| 2 | series | with the control device. | | |
| 1 | 11. | The apparatus defined by claim 9, wherein the varistor has cross- | | |
| 2 | bar ch | aracteristics. | | |

1 12. The apparatus defined by claim 7 or 8, wherein the two terminals of 2 the control device are an anode terminal and a cathode terminal, and the 3 variable resistor is a varistor, the varistor being coupled between the 4 anode terminal and gate of the control device.